

### **Remarks**

Several limitations have been added to independent claims 1, 12, 15, 18, 30, 39, 48, and 54 to clarify the scope of the claims. The limitation of having the header and payload in a single frame is supported in applicant's specification in many places, including Fig. 3 and at page 8 lines 18-19. The limitation of using different modulation rates for the header and payload may be found in many places, including page 9 line 22 through page 10 line 6. The limitation of switching to a different modulation rate has been removed as being possibly ambiguous, since it may be interpreted as requiring a switch, and limiting the claim to that specific physical implementation was never intended.

### **Conclusion**

If the Examiner has any questions concerning this application, he or she is requested to telephone the undersigned at the telephone number shown below as soon as possible. If any fee insufficiency or overpayment is found, please charge any insufficiency or credit any overpayment to Deposit Account No. 02-2666.

Respectfully submitted,

Intel Corporation

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